



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Xiaowei Deng
Serial No: 09/932,763
Examiner: Daniel D. Chang
Filed: 08/16/2001
For: SILICON-ON-INSULATOR DYNAMIC LOGIC

Docket No: TI-29320

Conf. No: 5414

Art Unit: 2819

**NOTICE OF APPEAL FROM THE PRIMARY EXAMINER
TO THE BOARD OF APPEALS**

Assistant Commissioner for Patents
Washington, DC 20231

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(a)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231 on 1-15-03.

Ann Trent
Ann Trent

Dear Sir:

Applicant hereby appeals to the Board of Appeals from the decision of the Primary Examiner mailed October 16, 2002, finally rejecting Claims 1-3, 5-7, 9-11 and 13-15.

The item(s) checked below are appropriate:

1. ☐ An extension of time to respond to the final rejection
☐ was granted on _____.
☐ is requested for ____ month.
2. ☒ A timely response to the final rejection has been filed.
3. ☒ Fee \$320.00:

☐ Not required (Fee paid in prior appeal)
☒ The Commissioner of Patents is hereby authorized to charge any fees which may be required or credit any overpayment to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.
This form is submitted in triplicate.

01/22/2003 AWONDAF1 00000131 200668 09932763

01 FC:1401 320.00 CH

Texas Instruments Incorporated
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AF/2800/K
#9/Notice of Appeal
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1/26/03

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